

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Suguru AKIYAMA, et al.**

Serial Number: **Not Yet Assigned**

Filed: **July 17, 2003**

For: **OPTICAL SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 17, 2003

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark

Office the reference listed on the attached PTO-1449.


A copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (1)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 030826	Serial No. New Application
	Applicant(s): Suguru AKIYAMA, et al.	
	Filing Date: July 17, 2003	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____	AE		
_____	AF		
_____	AG		
_____	AH		
_____	AI		

OTHER DOCUMENTS

_____	AJ	L. Mörl et al.; "A Travelling Wave Electrode Mach-Zehnder 40 Gb/s Demultiplexer Based on Strain Compensated GaInAs/AlInAs Tunnelling Barrier MQW Structure"; <i>10th Intern. Conf. on Indium Phosphide and Related Materials WA3-4</i> ; May 11-15, 1998; pp. 403-406.		
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<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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